

DECLARATION AND POWER OF ATTORNEY

As a below named joint inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention, design or discovery entitled "A CHARACTERISTIC IMPEDANCE EQUALIZER AND AN INTEGRATED CIRCUIT PACKAGE EMPLOYING THE SAME," the specification of which is attached hereto.

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above;

I acknowledge the duty to disclose to the Office all information known to me to be material to the patentability of this application as defined by Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

NUMBER	COUNTRY	DATE FILED	PRIORITY CLAIMED
	None		

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application(s) in the manner provided by the first paragraph of Title 35, United States Code § 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in § 1.56 which became available between the filing date of any prior application(s) and the national or PCT international filing date of this application.

<u>US/PCT Serial Number</u>	<u>Date Filed</u>	<u>Status</u>
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None		
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I hereby appoint:

Timothy R. Croll, Registration No. 36,771
Leo J. Peters, Registration No. 33,562
Peter P. Scott, Registration No. 33,279
Gary B. Goates, Registration No. 35,159
Sandeep Jaggi, Registration No. 43,331

of LSI Logic, Inc.

and I also hereby appoint:

David H. Hitt, Registration No. 33,182
Charles W. Gaines, Registration No. 36,804
Glenn W. Boisbrun, Registration No. 39,615
Robert R. Axenfeld, Registration No. 37,276
Mark E. Kelley, Registration No. 45,857
Greg H. Parker, Registration No. 44,995
Jimmy L. Heisz, Registration No. 38,914
James Joel Justiss, Registration No. P-48,981

of the firm of Hitt Gaines & Boisbrun, P.C., as my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent applications filed thereon before any international authorities under the Patent Cooperation Treaty.

Send correspondence to:

Leo J. Peters
LSI LOGIC CORPORATION
Mail Stop D-106; 1551 McCarthy Boulevard
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and direct all telephone calls to Leo J. Peters at (408) 433-4578.

Atty. Docket No.: 01-210

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first inventor: Anand Govind

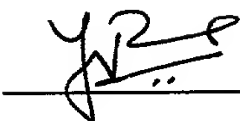
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Full name of second inventor: Yogendra Ranade

Inventor's signature:  Date: 8/13/2001

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